

L Number	Hits	Search Text	DB	Time stamp
1	2954	(714/30 or 714/25 or 714/733 or 714/734 or 700/26).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/10/20 16:08
2	1239	((714/30 or 714/25 or 714/733 or 714/734 or 700/26).ccls.) and chip	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/10/20 16:08
3	853	((714/30 or 714/25 or 714/733 or 714/734 or 700/26).ccls.) and chip) and (processor or controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/10/20 16:09
4	154	((714/30 or 714/25 or 714/733 or 714/734 or 700/26).ccls.) and chip) and (processor or controller)) and (program with count\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/10/20 16:09
5	143	((714/30 or 714/25 or 714/733 or 714/734 or 700/26).ccls.) and chip) and (processor or controller)) and (program with count\$3)) and register	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/10/20 16:09
6	141	((714/30 or 714/25 or 714/733 or 714/734 or 700/26).ccls.) and chip) and (processor or controller)) and (program with count\$3)) and register) and (peripheral or function\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/10/20 16:10
7	134	((714/30 or 714/25 or 714/733 or 714/734 or 700/26).ccls.) and chip) and (processor or controller)) and (program with count\$3)) and register) and (peripheral or function\$3)) and (select\$4 or multiplex\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/10/20 16:11
8	134	((714/30 or 714/25 or 714/733 or 714/734 or 700/26).ccls.) and chip) and (processor or controller)) and (program with count\$3)) and register) and (peripheral or function\$3)) and (select\$4 or multiplex\$3)) and (select\$4 control\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/10/20 16:11
9	103	((714/30 or 714/25 or 714/733 or 714/734 or 700/26).ccls.) and chip) and (processor or controller)) and (program with count\$3)) and register) and (peripheral or function\$3)) and (select\$4 or multiplex\$3)) and (select\$4 with control\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/10/20 16:12
10	92	((714/30 or 714/25 or 714/733 or 714/734 or 700/26).ccls.) and chip) and (processor or controller)) and (program with count\$3)) and register) and (peripheral or function\$3)) and (select\$4 or multiplex\$3)) and (select\$4 with control\$3)) and @ad<20011012	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/10/20 16:27
11	88	((714/30 or 714/25 or 714/733 or 714/734 or 700/26).ccls.) and chip) and (processor or controller)) and (program with count\$3)) and register) and (peripheral or function\$3)) and (select\$4 or multiplex\$3)) and (select\$4 with control\$3)) and @ad<20010710	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/10/20 16:27